

Inventor Information for 10/671934

Inventor Name	City	State/Country
GUSTAVSON, FRED GEHRUNG	BRIARCLIFF MANOR	NEW YORK
GUNNELS, JOHN A.	MT. KISCO	NEW YORK

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Last Name = GUSTAVSON

First Name = FRED

Application#	Patent#	Status	Date Filed	Title	Inventor Name
09918814	6842765	150	07/31/2001	PROCESSOR DESIGN FOR EXTENDED-PRECISION ARITHMETIC	GUSTAVSON, FRED G.
08217533	Not Issued	161	03/24/1994	NUMERICALLY INTENSIVE COMPUTER ACCELERATOR	GUSTAVSON, FRED G.
08313970	5758176	250	09/28/1994	METHOD AND SYSTEM FOR PROVIDING A SINGLE-INSTRUCTION, MULTIPLE-DATA EXECUTION UNIT FOR PERFORMING SINGLE-INSTRUCTION, MULTIPLE-DATA OPERATIONS WITHIN A SUPERSCALAR DATA PROCESSING SYSTEM	GUSTAVSON, FRED G.
08313971	5513366	150	09/28/1994	METHOD AND SYSTEM FOR DYNAMICALLY RECONFIGURING A REGISTER FILE IN A VECTOR PROCESSOR	GUSTAVSON, FRED G.
08354402	5563818	150	12/12/1994	A METHOD AND SYSTEM FOR PERFORMING FLOATING-POINT DIVISION USING SELECTED APPROXIMATION VALUES	GUSTAVSON, FRED G.
08368171	Not Issued	166	01/04/1995	METHOD AND SYSTEM FOR ADDRESSING REGISTERS IN A DATA PROCESSING UNIT IN AN INDEXED ADDRESSING MODE	GUSTAVSON, FRED G.
08368172	5680338	250	01/04/1995	METHOD AND SYSTEM FOR VECTOR PROCESSING UTILIZING SELECTED VECTOR ELEMENTS	GUSTAVSON, FRED G.
08368173	5887183	150	01/04/1995	METHOD AND SYSTEM IN A DATA PROCESSING SYSTEM FOR LOADING AND STORING VECTORS IN A PLURALITY OF MODES	GUSTAVSON, FRED G.
08368176	5890222	150	01/04/1995	METHOD AND SYSTEM FOR ADDRESSING REGISTERS IN A DATA PROCESSING UNIT IN AN INDIRECT ADDRESSING MODE	GUSTAVSON, FRED G.
08589770	5751619	250	01/22/1996	RECURRENT ADRITHMETICAL COMPUTATION USING CARRY-SAVE ARITHMETIC	GUSTAVSON, FRED G.

<u>08619456</u>	<u>5825677</u>	250	03/20/1996	NUMERICALLY INTENSIVE COMPUTER ACCELERATOR	GUSTAVSON, FRED G.
<u>08687825</u>	<u>5832533</u>	250	07/26/1996	METHOD AND SYSTEM FOR ADDRESSING REGISTERS IN A DATA PROCESSING UNIT IN AN INDEXED ADDRESSING MODE	GUSTAVSON, FRED G.
<u>10671887</u>	Not Issued	71	09/29/2003	Method and structure for producing high performance linear algebra routines using composite blocking based on L1 cache size	GUSTAVSON, FRED GEHRUNG
<u>10671888</u>	Not Issued	41	09/29/2003	Method and structure for producing high performance linear algebra routines using register block data format routines	GUSTAVSON, FRED GEHRUNG
<u>10671889</u>	Not Issued	25	09/29/2003	Method and structure for producing high performance linear algebra routines using level 3 prefetching for kernel routines	GUSTAVSON, FRED GEHRUNG
<u>10671933</u>	Not Issued	30	09/29/2003	Method and structure for producing high performance linear algebra routines using a hybrid full-packed storage format	GUSTAVSON, FRED GEHRUNG
<u>10671934</u>	Not Issued	30	09/29/2003	Method and structure for producing high performance linear algebra routines using streaming	GUSTAVSON, FRED GEHRUNG
<u>10671935</u>	Not Issued	25	09/29/2003	Method and structure for producing high performance linear algebra routines using a selectable one of six possible level 3 L1 kernel routines	GUSTAVSON, FRED GEHRUNG
<u>10671937</u>	Not Issued	25	09/29/2003	Method and structure for producing high performance linear algebra routines using preloading of floating point registers	GUSTAVSON, FRED GEHRUNG
<u>11035902</u>	Not Issued	25	01/14/2005	Method and structure for a generalized cache-register file interface with data restructuring methods for multiple cache levels and hardware pre-fetching	GUSTAVSON, FRED GEHRUNG
<u>11035933</u>	Not Issued	30	01/14/2005	Method and structure for cache aware transposition via rectangular subsections	GUSTAVSON, FRED GEHRUNG
<u>11039907</u>	Not Issued	30	01/24/2005	Method and structure for algorithmic overlap in parallel processing for exploitation when load imbalance is dynamic and predictable	GUSTAVSON, FRED GEHRUNG
<u>11045354</u>	Not Issued	25	01/31/2005	Method and structure for a hybrid full- packed storage format as a single rectangular format data structure	GUSTAVSON, FRED GEHRUNG
<u>11050945</u>	Not Issued	30	02/07/2005	System and method for detecting a faulty object in a system	GUSTAVSON, FRED GEHRUNG
<u>11052877</u>	Not Issued	30	02/09/2005	System and method for algorithmic cache- bypass	GUSTAVSON, FRED GEHRUNG
<u>11133254</u>	Not Issued	25	05/20/2005	Method and structure for improving processing efficiency in parallel processing machines for rectangular and triangular	GUSTAVSON, FRED GEHRUNG

				matrix routines	
<u>11328344</u>	Not Issued	30	01/09/2006	Method and structure for an improved data reformatting procedure	GUSTAVSON, FRED GEHRUNG

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Application#	Patent#	Status	Date Filed	Title	Inventor Name
10671887	Not Issued	71	09/29/2003	Method and structure for producing high performance linear algebra routines using composite blocking based on L1 cache size	GUNNELS, JOHN A.
10671888	Not Issued	41	09/29/2003	Method and structure for producing high performance linear algebra routines using register block data format routines	GUNNELS, JOHN A.
10671889	Not Issued	25	09/29/2003	Method and structure for producing high performance linear algebra routines using level 3 prefetching for kernel routines	GUNNELS, JOHN A.
10671933	Not Issued	30	09/29/2003	Method and structure for producing high performance linear algebra routines using a hybrid full-packed storage format	GUNNELS, JOHN A.
10671934	Not Issued	30	09/29/2003	Method and structure for producing high performance linear algebra routines using streaming	GUNNELS, JOHN A.
10671935	Not Issued	25	09/29/2003	Method and structure for producing high performance linear algebra routines using a selectable one of six possible level 3 L1 kernel routines	GUNNELS, JOHN A.
10671937	Not Issued	25	09/29/2003	Method and structure for producing high performance linear algebra routines using preloading of floating point registers	GUNNELS, JOHN A.
11035902	Not Issued	25	01/14/2005	Method and structure for a generalized cache-register file interface with data restructuring methods for multiple cache levels and hardware pre-fetching	GUNNELS, JOHN A.
11035933	Not Issued	30	01/14/2005	Method and structure for cache aware transposition via rectangular subsections	GUNNELS, JOHN A.
11039907	Not Issued	30	01/24/2005	Method and structure for algorithmic overlap in parallel processing for exploitation when load imbalance is dynamic and predictable	GUNNELS, JOHN A.
11041935	Not Issued	30	01/26/2005	Method and structure for high-performance linear algebra in the presence of limited outstanding miss slots	GUNNELS, JOHN A.
11045354	Not Issued	25	01/31/2005	Method and structure for a hybrid full-packed storage format as a single	GUNNELS, JOHN A.

				rectangular format data structure	
11050945	Not Issued	30	02/07/2005	System and method for detecting a faulty object in a system	GUNNELS, JOHN A.
11052216	Not Issued	30	02/08/2005	Method and structure for skewed block-cyclic distribution of lower-dimensional data arrays in higher-dimensional processor grids	GUNNELS, JOHN A.
11052877	Not Issued	30	02/09/2005	System and method for algorithmic cache-bypass	GUNNELS, JOHN A.
11133254	Not Issued	25	05/20/2005	Method and structure for improving processing efficiency in parallel processing machines for rectangular and triangular matrix routines	GUNNELS, JOHN A.
11328344	Not Issued	30	01/09/2006	Method and structure for an improved data reformatting procedure	GUNNELS, JOHN A.
60377710	Not Issued	159	05/03/2002	System and method for performing linear algebra	GUNNELS, JOHN A.

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